

LOGICAL CIRCUIT DESIGN APPARATUS 50

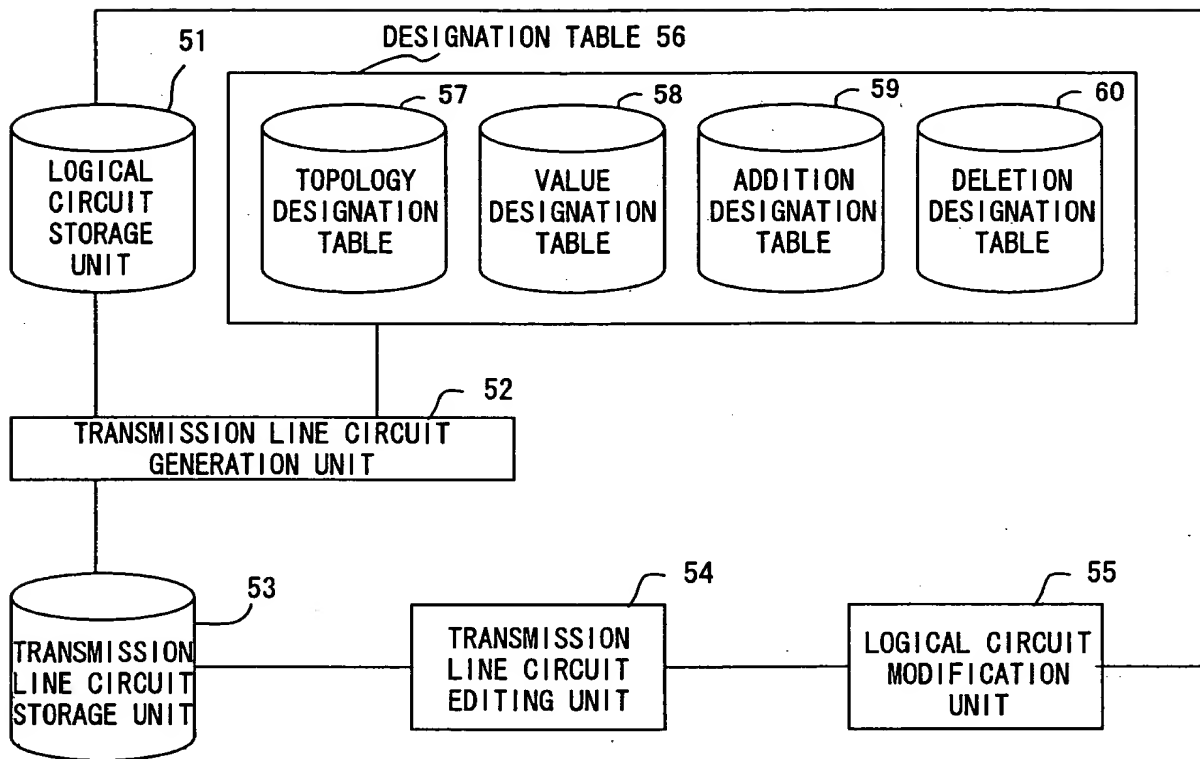


FIG. 1

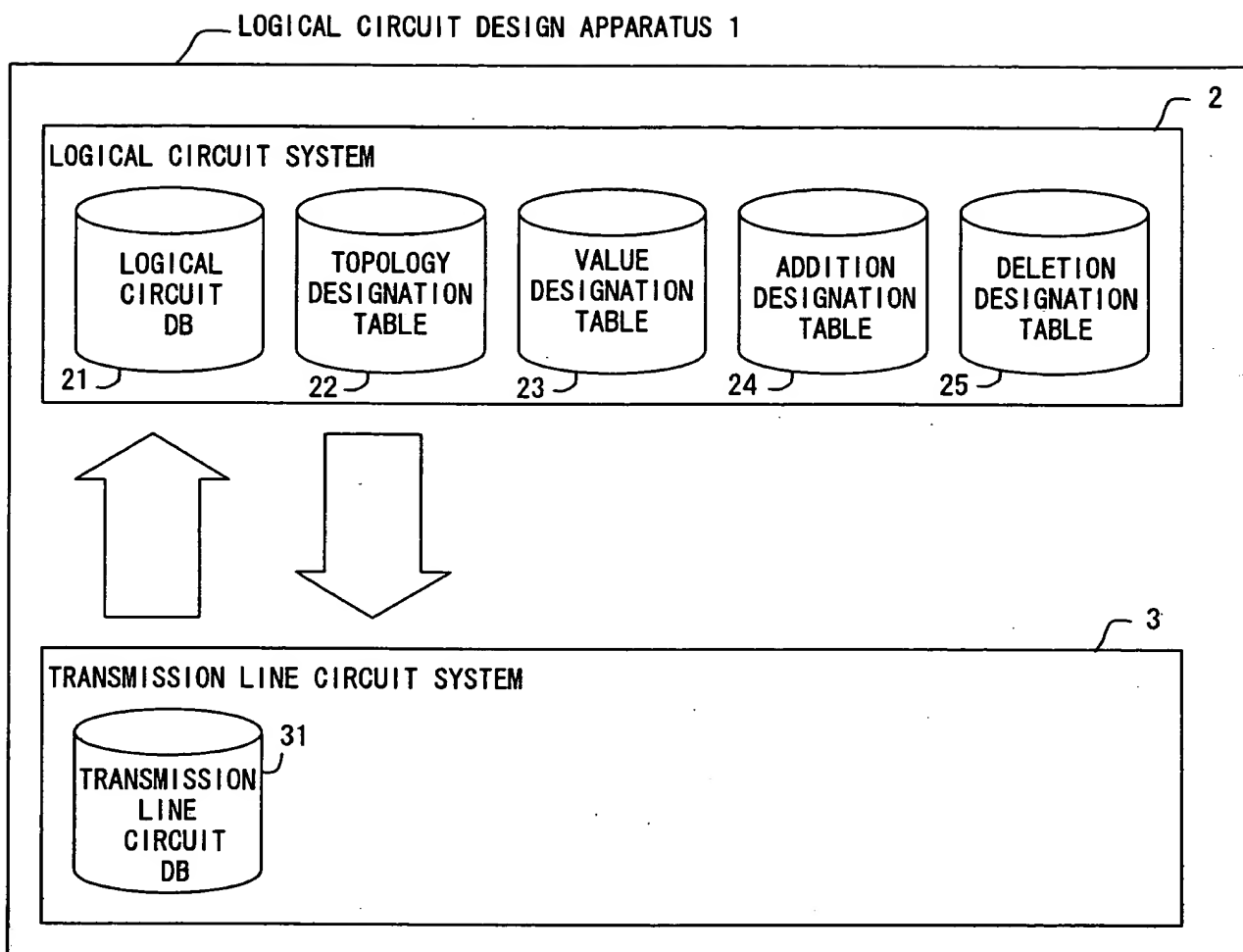


FIG. 2

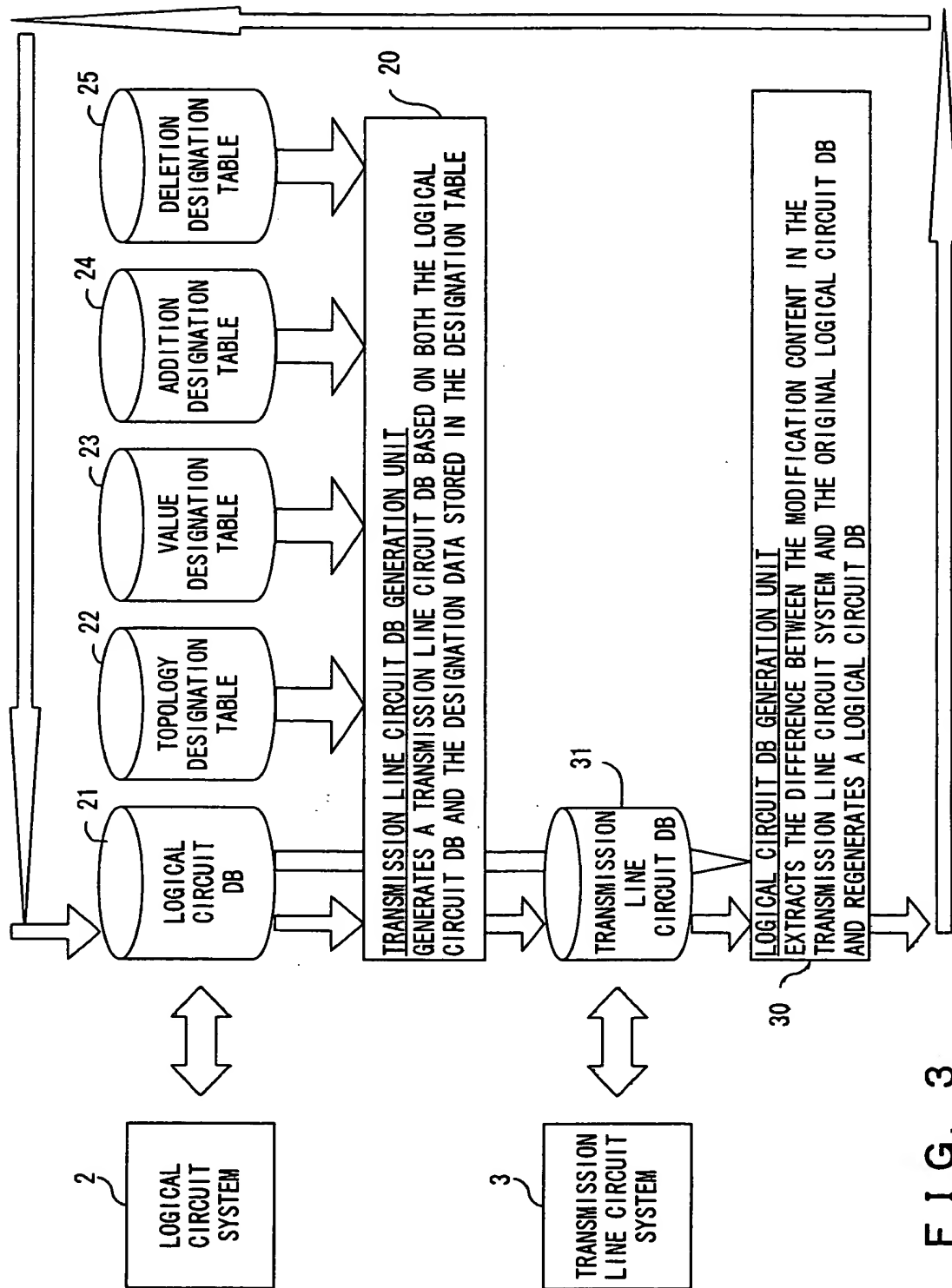
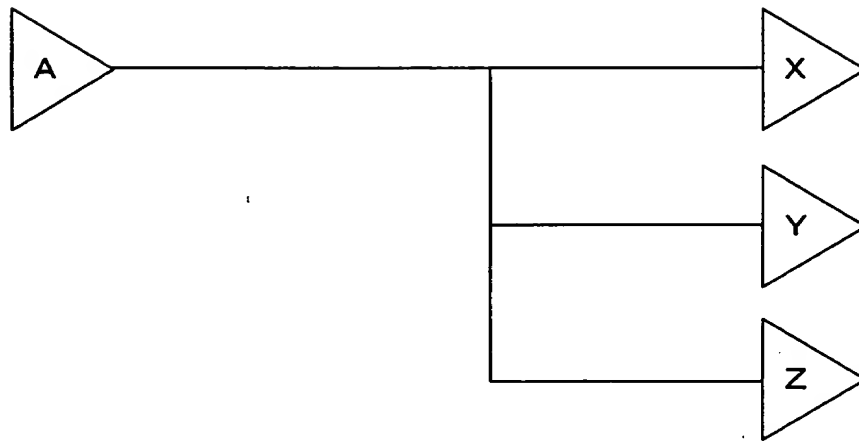


FIG. 3



F I G. 4

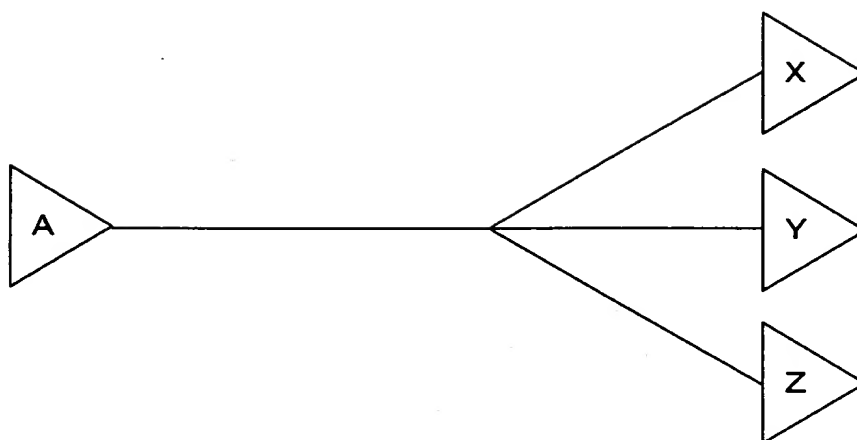


FIG. 5

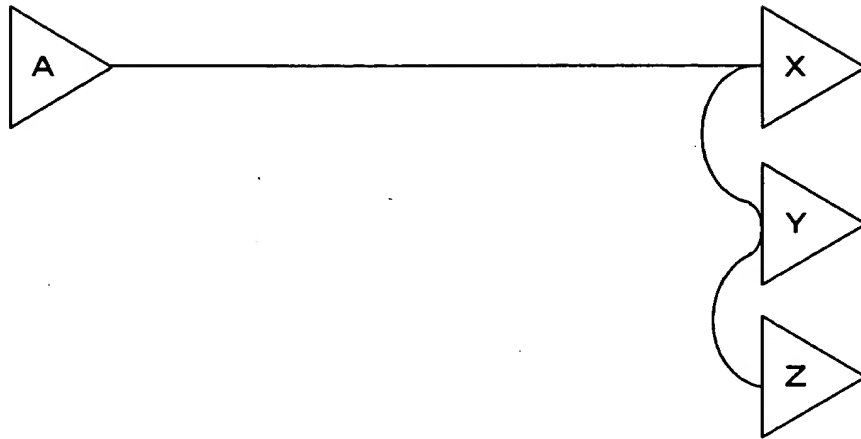


FIG. 6

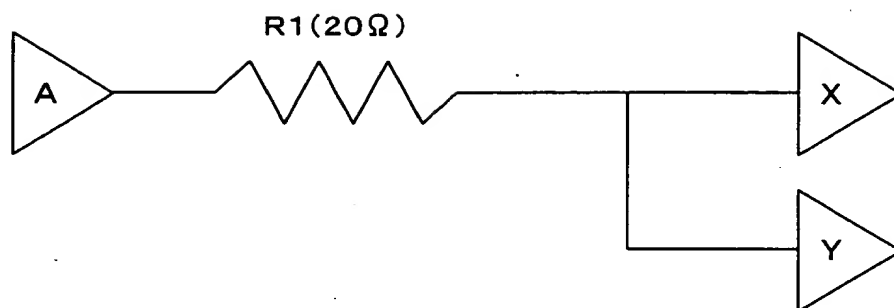


FIG. 7

FIG. 8

ITEM NO.	SYMBOL	COMPONENT DESCRIPTION	VALUE
1	R1	RESISTOR	33 Ω

FIG. 8

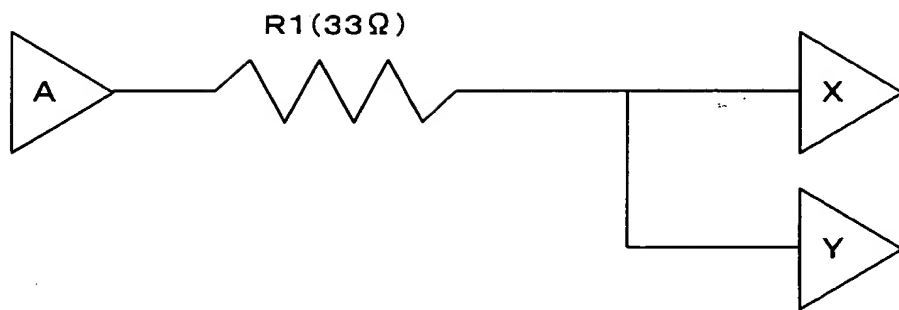


FIG. 9

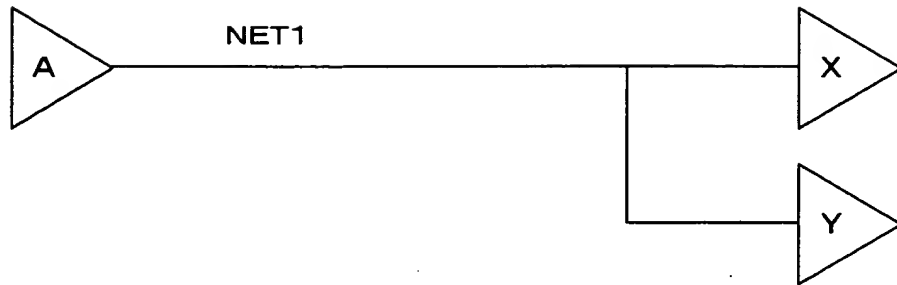


FIG. 10

FIG. 11

ITEM NO.	NET	LOCATION	COMPONENT DESCRIPTION	VALUE
1	NET1	BY THE SIDE OF A	RESISTOR	33 Ω

FIG. 11

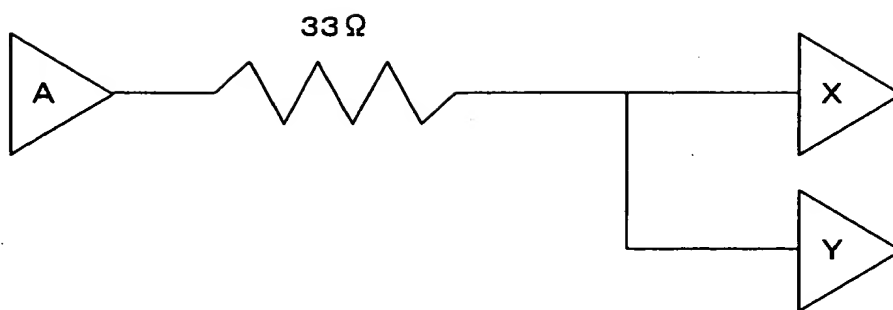


FIG. 12

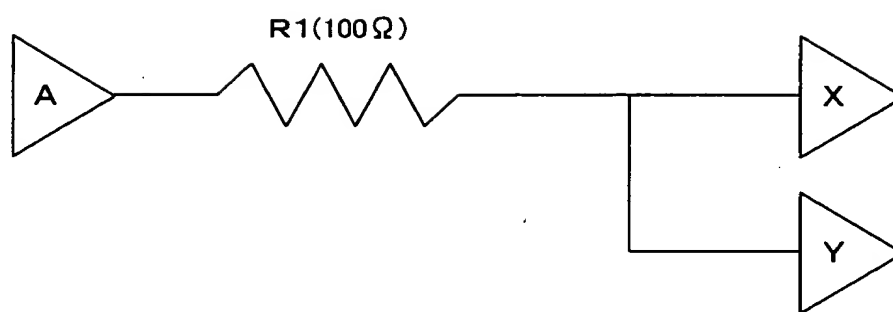


FIG. 13

ITEM NO.	SYMBOL
1	R1

FIG. 14

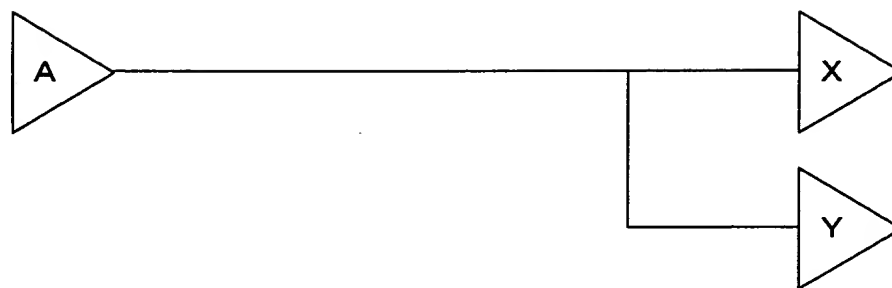


FIG. 15

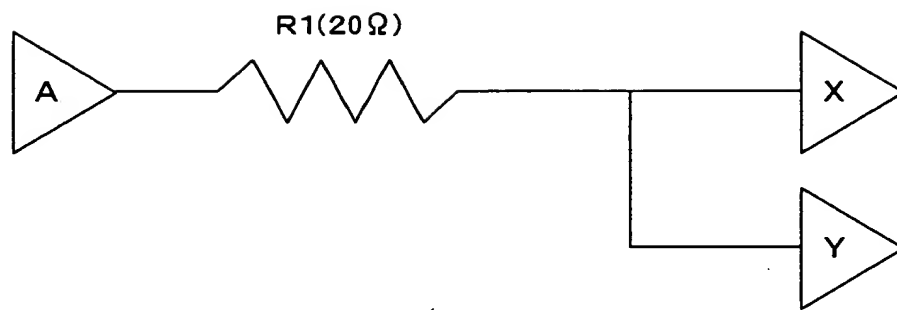
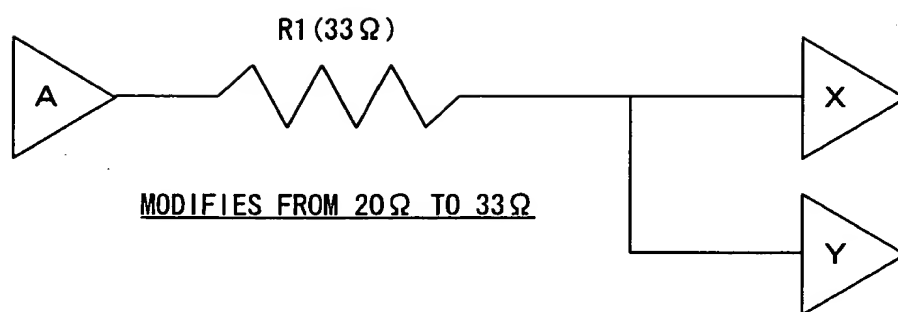


FIG. 16



F I G. 1 7

FIG. 1030300

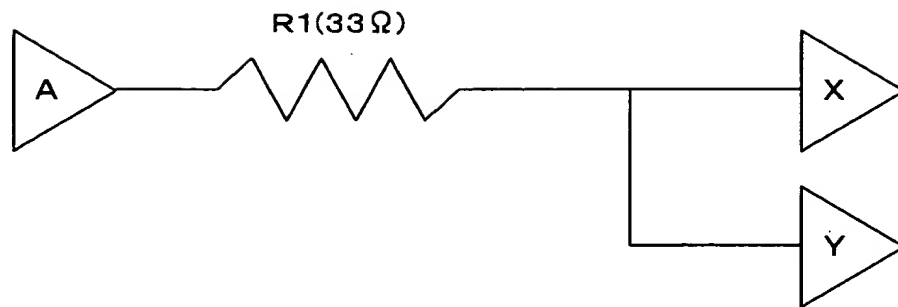


FIG. 18

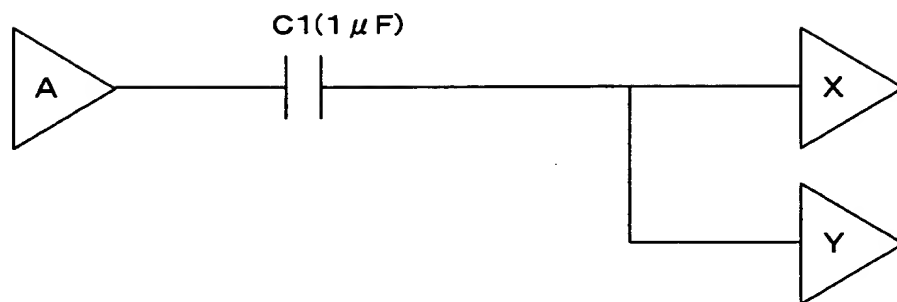


FIG. 19

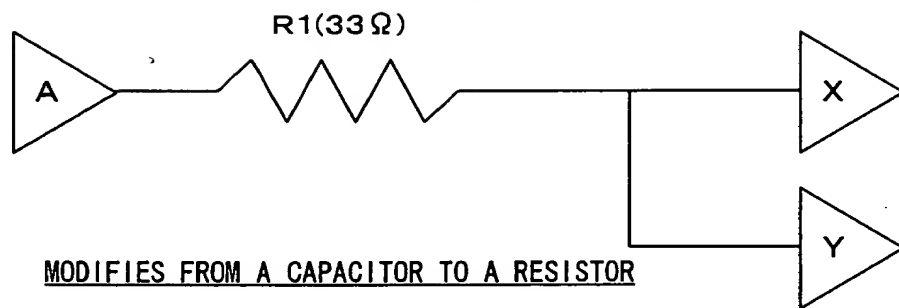
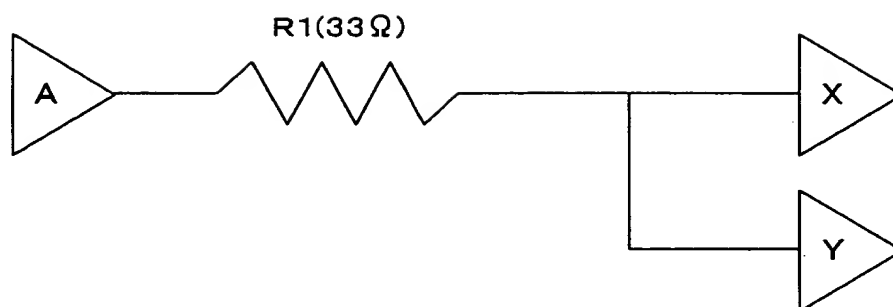


FIG. 20



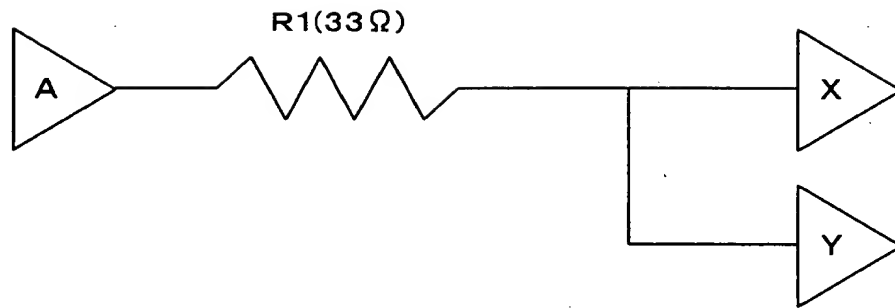


FIG. 22

404260-1050960

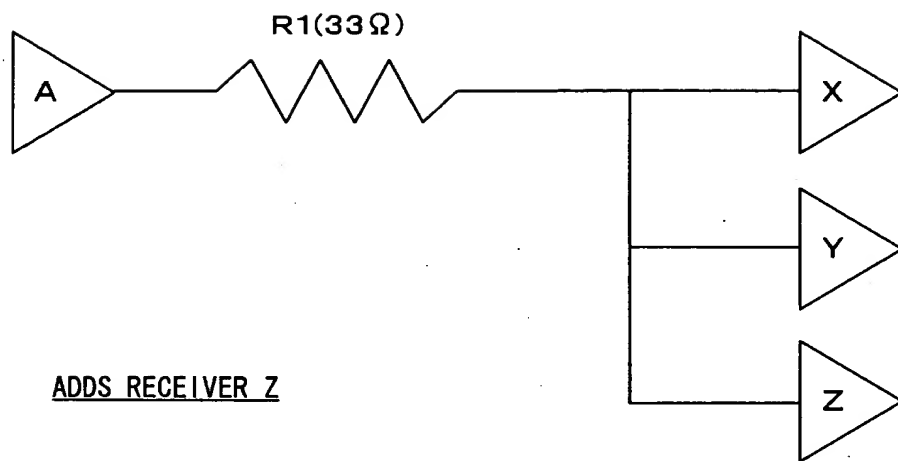


FIG. 23

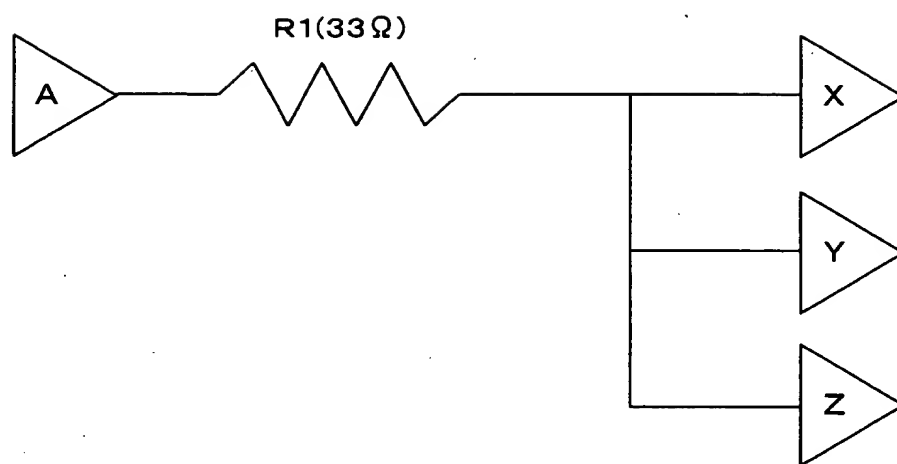


FIG. 24

FIG. 25

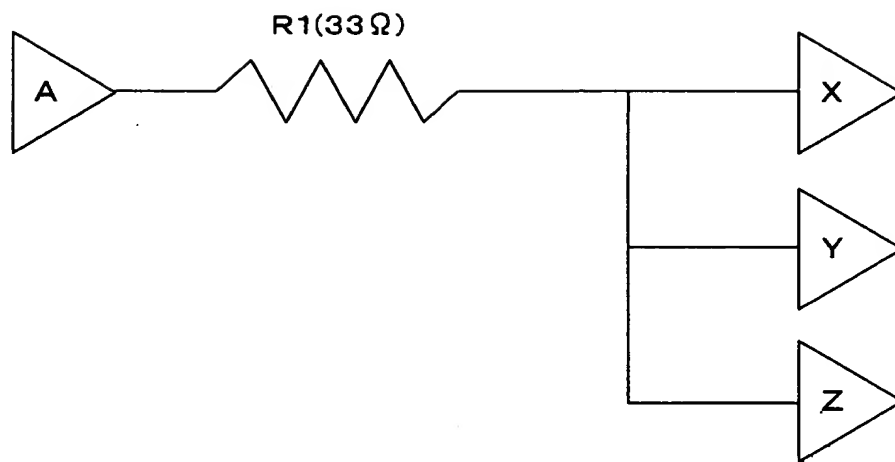


FIG. 25



FIG. 26

000000-000000

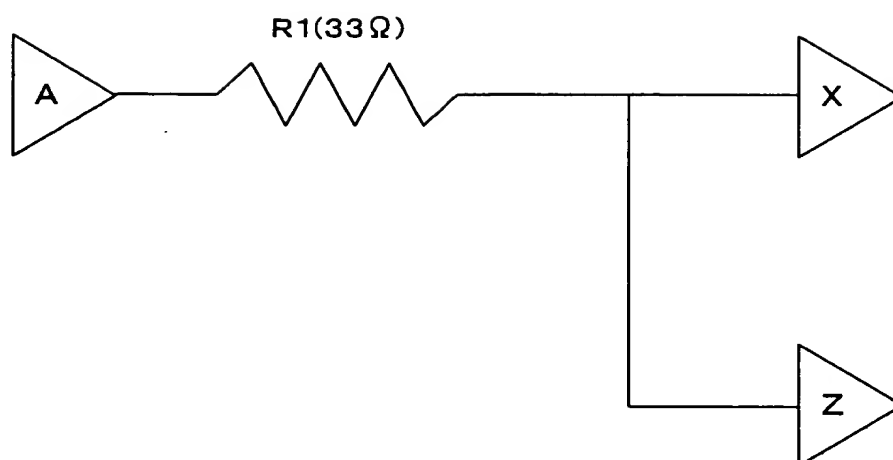


FIG. 27

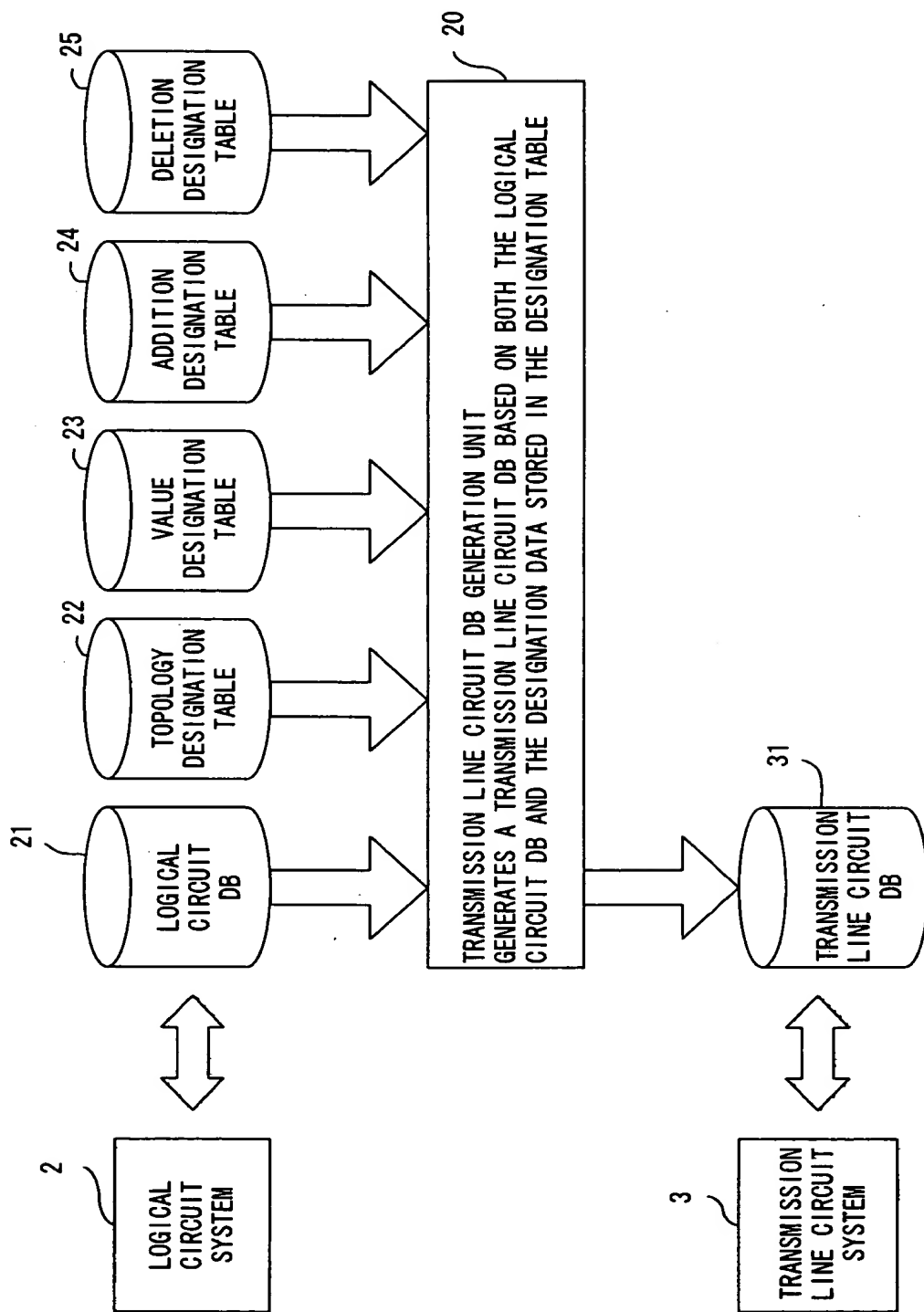


FIG. 28

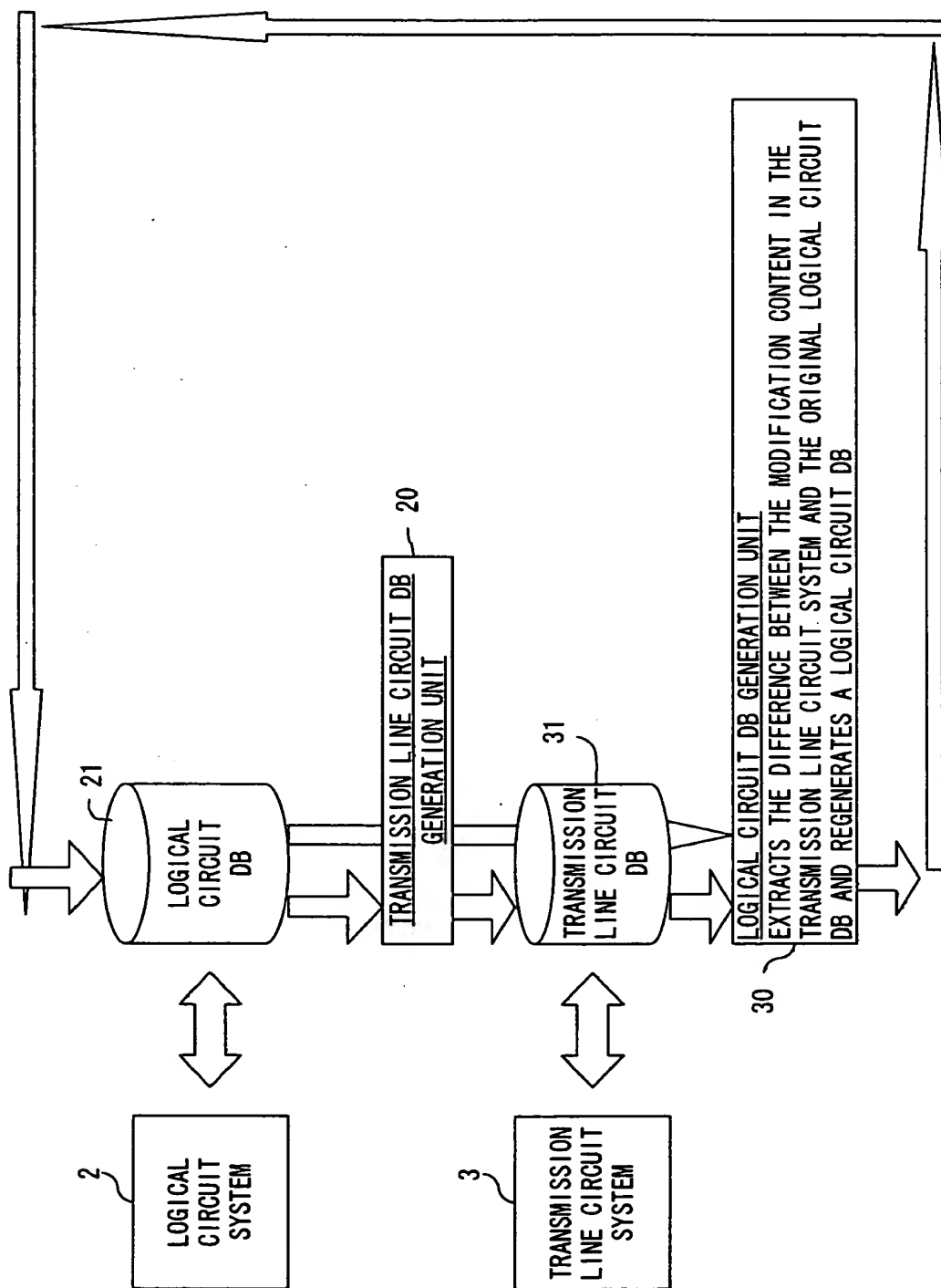


FIG. 29

FIG. 30

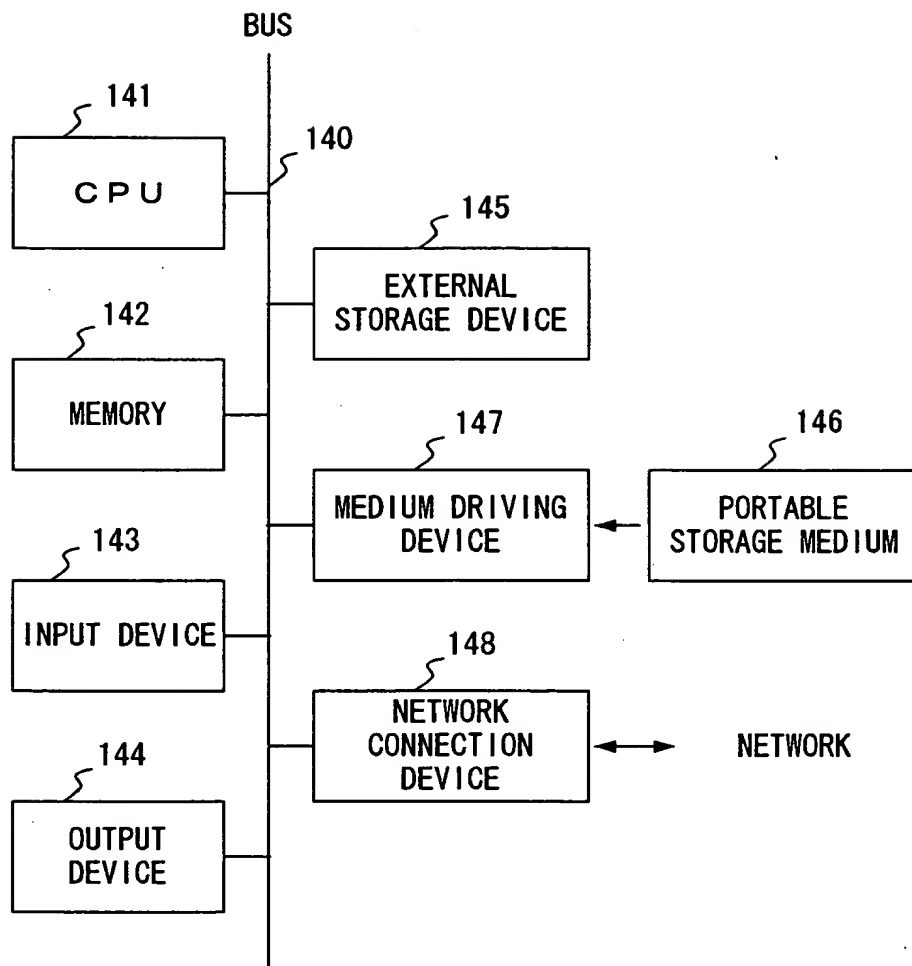


FIG. 30

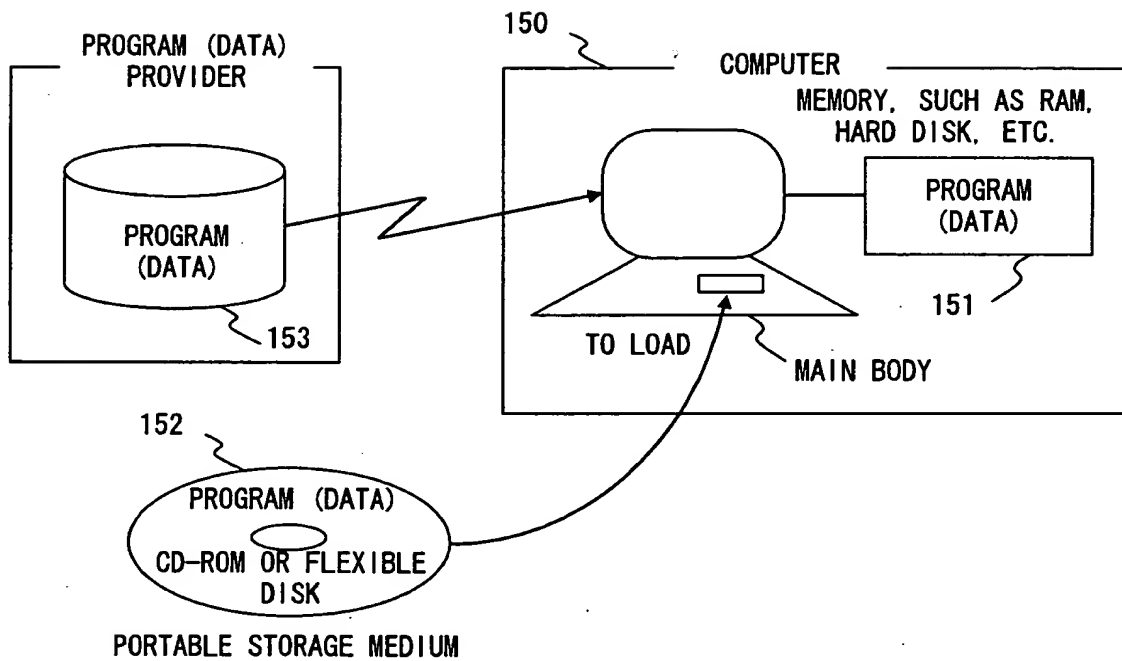


FIG. 31